INFORMATION DISCLOSURE CITATION IN AN APPLICATION			Docket Number (Optional) 81790.0297		Application Number Not assigned	
			Applicant  Daisuke KATO et al.			
(Use several sheets if necessary)			Filing Date September 17, 2003		Group Art Unit 78 1/8	
	·	J.S. PATEN	T DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
VV	5,546,349	8/13/96	WATANABE et al.	-		
W	5,615,164	3/25/97	KIRIHATA et al.			
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	VIET Q. NGUYEN PRIMARY EXAMINE					
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	OTHER DOCUMENTS	(Including A	uthor, Title, Date, Perti	nent Pages,	Etc.)	
W	Yohji WATANABE et al., "A 286 mm² 256Mb DRAM with x32 Both-Ends DQ", IEEE Journal of Solid-State Circuits, Vol. 31, No. 4, April 1996, pp 567-574.					
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			VIETO NGUVEN		•	
	PRIMARY EXAMINER					
	- WILLIAM TO					
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EXAMINER V. NORWEGEN DATE CONSIDERED 11/2/04						
EXAMINER: I	nitial if citation considered, whether or nand not considered. Include copy of this	ot citation is ir s form with ne	conformance with MPE at communication to the	P § 609; Dra	w line through ci	tation if not in